

**Notice of References Cited**

Application/Control No.

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Applicant(s)/Patent Under  
Reexamination  
GRASSL, THOMAS

Examiner

David L. Hogans

Art Unit

2813

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,426,072	06-1995	Finnila, Ronald M.	438/107
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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	N					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era (2000), Lattice Press, Volume 1, Second Edition, pages 268-269.
	V	Quirk et al., Semiconductor Manufacturing Technology (2001), Prentice Hall, page 301.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.